	L #	Hits	Search Text	DBs
1	L1	477	710/306.ccls.	US-PGPUB; USPAT
2	L2	0	1 and (bridge$1 with (micro$1processor processor) with (memory adj bus) with (memory adj controller))	US-PGPUB; USPAT
3	L3	310	1 and bridge	US-PGPUB; USPAT
4	L4	243	3 and (memory and controller and bus)	US-PGPUB; USPAT
5	L5	79	4 and keyboard	US-PGPUB; USPAT